

WEST Search History

DATE: Tuesday, September 27, 2005

Hide?	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L37	L9 and legal\$	12
<input type="checkbox"/>	L36	L9 and illegal\$	2
<input type="checkbox"/>	L35	L33 and cost	30
<input type="checkbox"/>	L34	L33 and ("logic array block" or "logic-array block" or lab)	25
<input type="checkbox"/>	L33	L23 and L32	46
<input type="checkbox"/>	L32	(place? or placing) near2 ("logic element" or le)	203
<input type="checkbox"/>	L31	(3617714 4706216 4870302 4890238 4908772 4967367 5189628 5224056 5341308)! [pn]	9
<input type="checkbox"/>	L30	L27 and "cost function"	16
<input type="checkbox"/>	L29	L28 and "cost function"	1
<input type="checkbox"/>	L28	L27 and illegal\$	3
<input type="checkbox"/>	L27	L24 and ("logic array block" or "logic-array block" or lab)	334
<input type="checkbox"/>	L26	L25 and "cost function"	7
<input type="checkbox"/>	L25	L24 and illegal\$	121
<input type="checkbox"/>	L24	L23 and ("logic element" or le)	2800
<input type="checkbox"/>	L23	"field programmable gate array" or fpga	21632
<input type="checkbox"/>	L22	("logic-array block" or "logic array block" or lab) near5 (adjacent)	229
<input type="checkbox"/>	L21	("logic-array block" or "logic array block" or lab) near5 (sibling)	0
<input type="checkbox"/>	L20	("logic-array block" or "logic array block" or lab) near5 (fanout or fan-out)	5
<input type="checkbox"/>	L19	("logic-array block" or "logic array block" or lab) near5 (fanin or fan-in)	3
<input type="checkbox"/>	L18	L17	0
<input type="checkbox"/>	L17	("logic-array block" or "logic array block" or lab) near5 (fanin and fanout and sibling and adjacent)	0
<input type="checkbox"/>	L16	L15 and L8	8
<input type="checkbox"/>	L15	incremental\$ near2 placement	91
<input type="checkbox"/>	L14	(new adj ("logic element" or le))	39
<input type="checkbox"/>	L13	L8 and (new adj ("logic element" or le))	2
<input type="checkbox"/>	L12	5521837[uref]	76
<input type="checkbox"/>	L11	5341308[pn]	1
<input type="checkbox"/>	L10	5341305[pn]	1
<input type="checkbox"/>	L9	L8 and (plac\$ near5 ("logic element" or le))	91

☐ L8 "field programmable gate array" or fpga

21632

END OF SEARCH HISTORY